



JPW
PATENT

Docket No.: ACT-368

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Art Unit: 2822

Examiner: Soward, Ida M.

Serial No.: 10/629,337

Filed: July 28, 2003

In re Application of: John McCollum

For: TRANSISTOR HAVING FULLY-DEPLETED JUNCTIONS TO REDUCE
CAPACITANCE AND INCREASE RADIATION IMMUNITY IN AN
INTEGRATED CIRCUIT

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail, in an envelope addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on 2/21/06.

Signed Krista Thompson
Krista Thompson

TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find a Response to the Office Action dated December 17, 2004 in connection with the above-identified application.

In the event the patent office charges a fee for filing the above-noted documents, including any fees required under 37 CFR 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, the Assistant Commissioner is hereby authorized to charge or credit the difference to our Deposit Account No. 50-0612. An additional copy of this page is enclosed.

Respectfully submitted,
SIERRA PATENT GROUP, LTD.

Jonathan D. Hanish
Reg. No.: 57,821

Dated: February 21, 2006

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Signed *Quo Thompson*

RESPONSE TO OFFICE ACTION

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated November 21, 2005, kindly consider the following remarks.